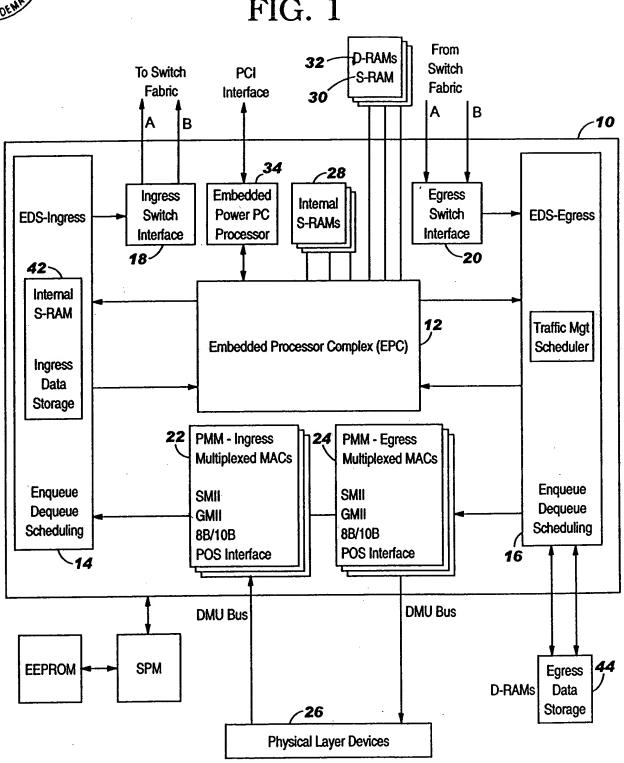
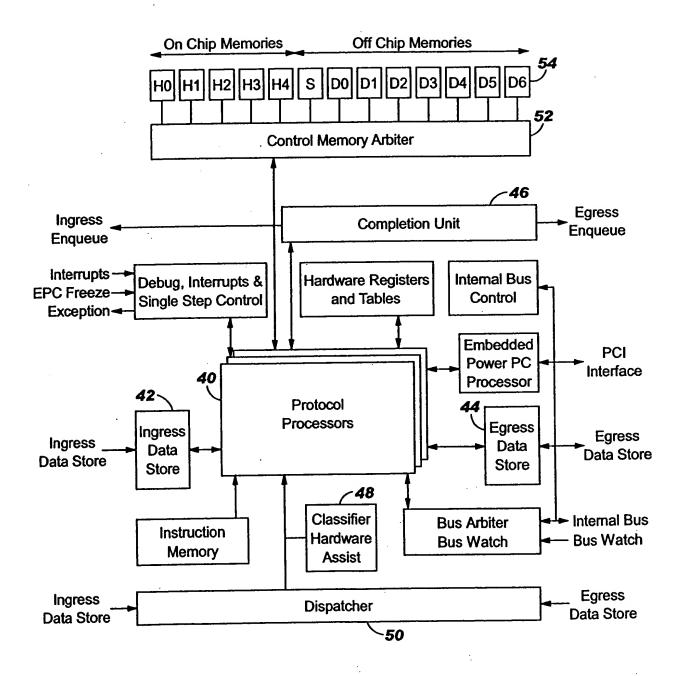


1/14 FIG. 1

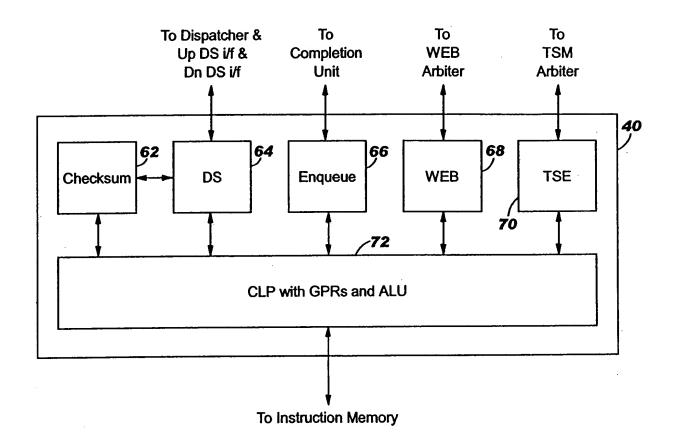


2/14 FIG. 2

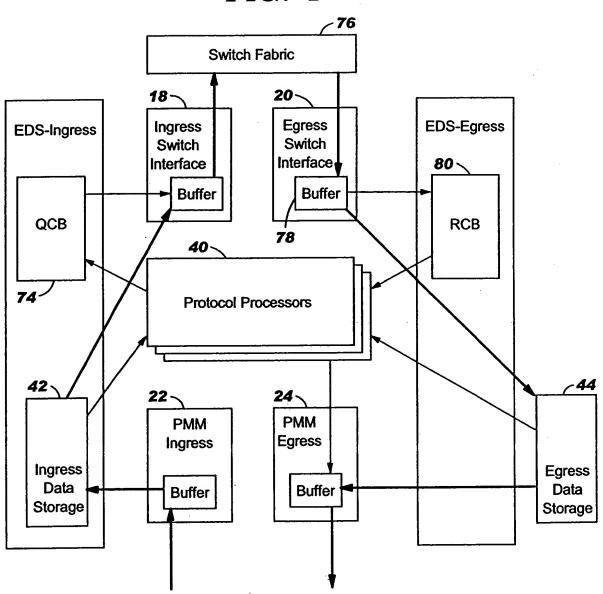


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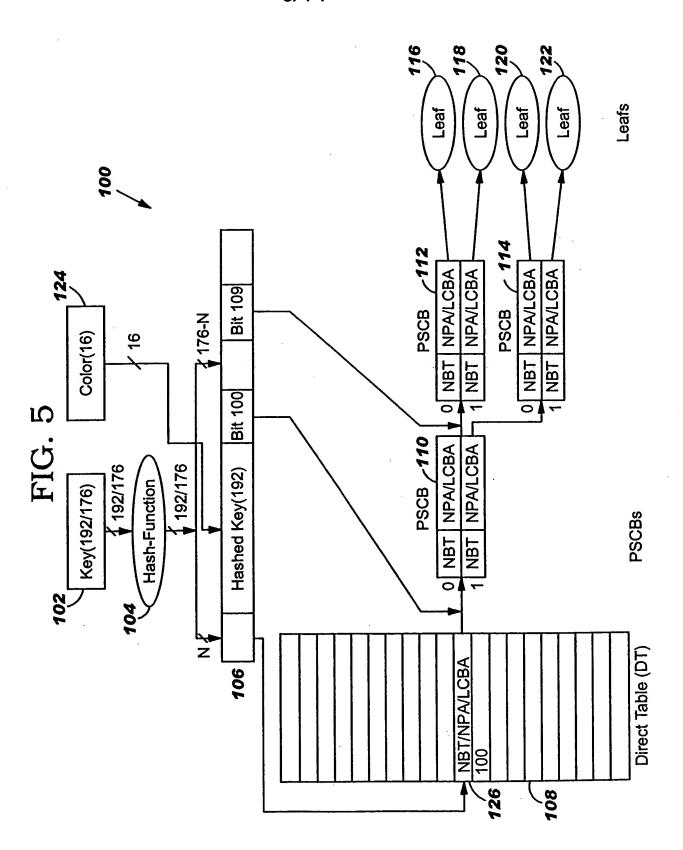
FIG. 3

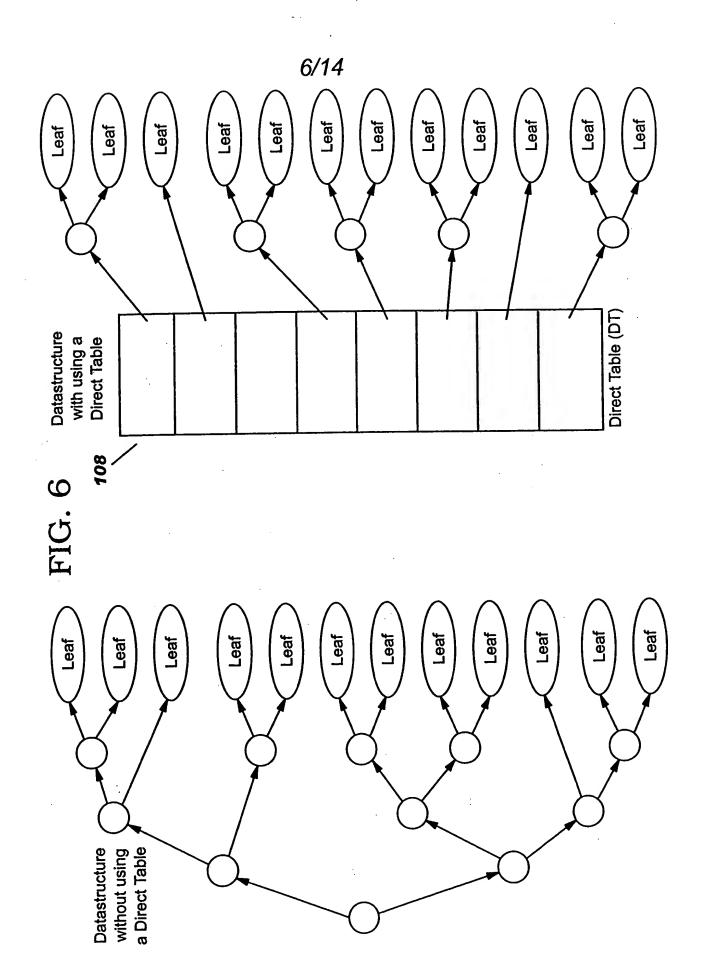


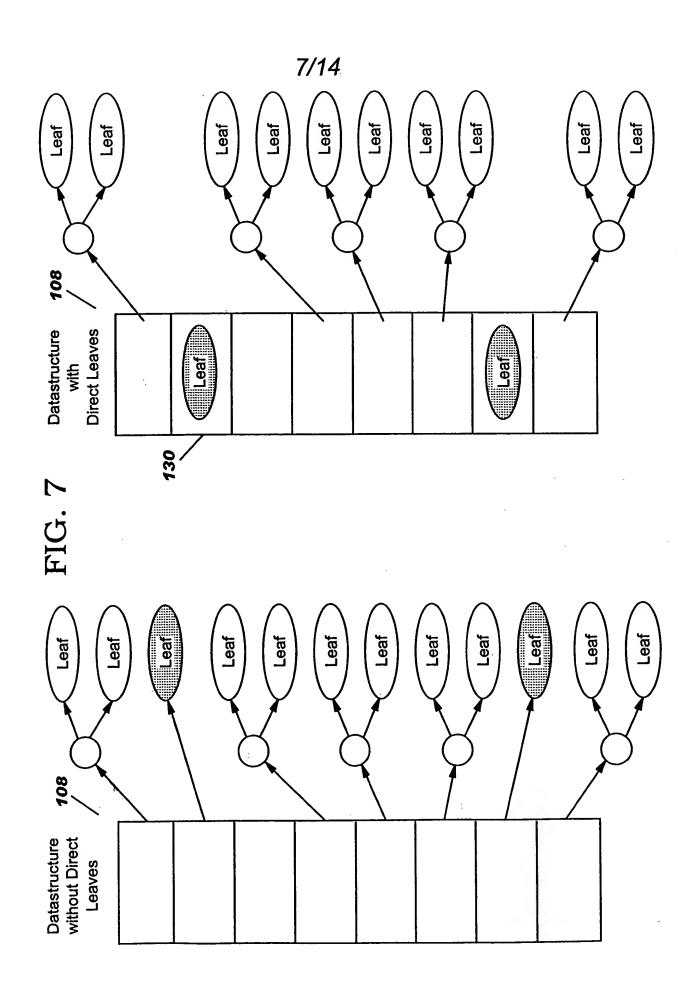
4/14 FIG. 4



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Format	Conditions	Valid in	Valid in	Format	NPA	NBT	LCBA	Spare	
		DTEntry?	PSCB?	(2 bits)	(26 bits)	(8 bits)	(26 bits)	(2 bits)	
Empty DTEntry	No leaves	Yes	Yes	00	0	0	0	0	
LCBA not	DTEntry contains pointer	Yes	Yes	00	NPA	NBT	0	0	
valid	to PSCB								
LCBA	Single leaf associated with	Yes	Yes	01	0	0	LCBA	0	
valid;	DTEntry; LCBA contains								
NPANBT	pointer into leaf; No								
not valid	pointer to next PSCB								
LCBA	Single leaf associated with	Yes	Xes	10	NPA	NBT	LCBA	0	
valid;	DTEntry; LCBA contains								
NPAMBT	pointer to leaf; Pointer to								
valid	next PSCB					:			

FIG. 8

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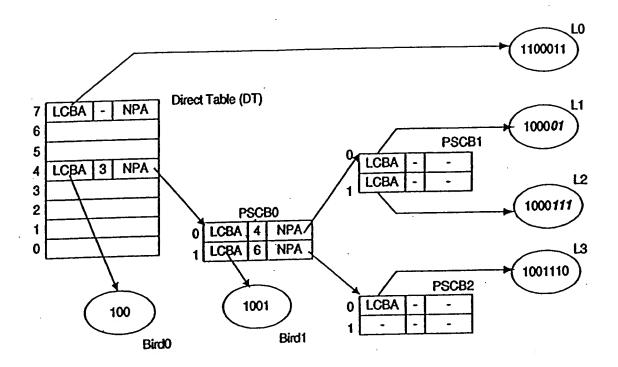


FIG. 9

Input Key	Input Key Length	Pattern stored in the leaf	Leaf-pattern Length	DISP
10011	5	1011010	7	2
10011	5	1001101	7	5
1011010	7	10011	5	2

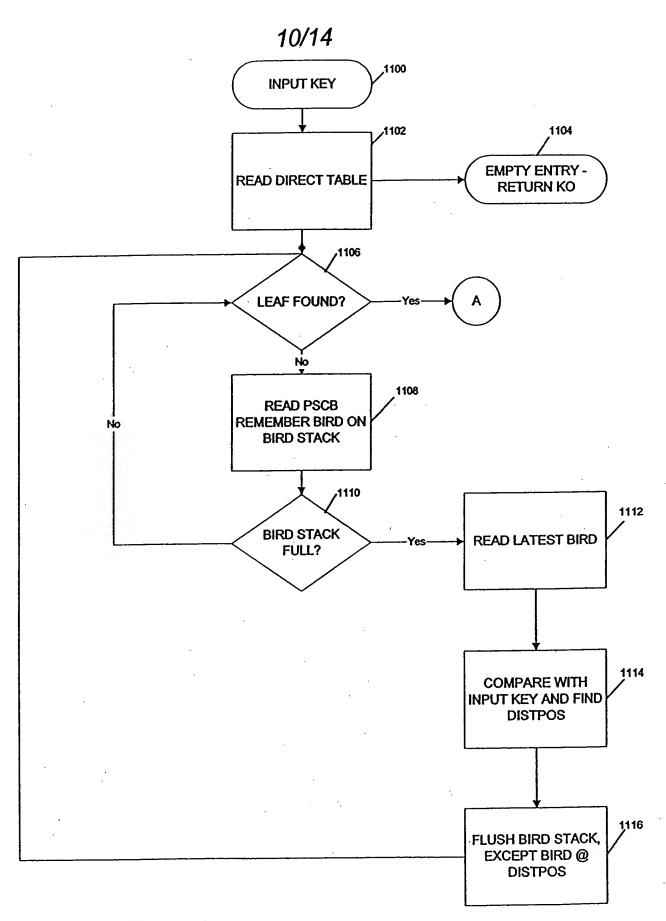
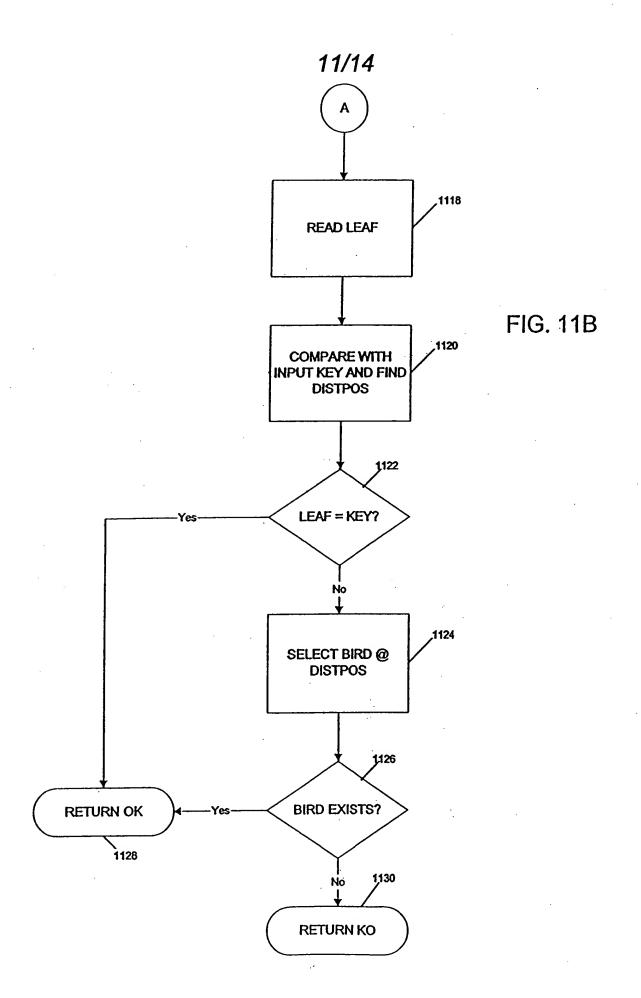


FIG. 11A



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FIG. 12

LUDefTable Tree Definition

Field	Size	Bits
CacheEntry	1	0
Tree_Type	2	21
hash-type	4	63
color_en	1	7
P1P2_max_size	5	128
NPARope_en	1	13
NPASMT_en	11	14
Complndex en	1	15
PSCB fq index	6	2116
PSCB Height	1	22
Mask_Vector_En	1	23
Complndex	8	3124
DT_base_addr	26	5732
DT_size	4 /	6158
DT_interleaf	2	6362
Leaf_fq_index	6	6964
Leaf_Width	2	7170
Leaf_Height	3	7472
DirectLeafEn	1	75

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RAL919990140US:1
Longest Prefix Match (LPM) Algorithm Implementation
For A Network Processor
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FIG. 13

Field	Size	Address in TSM where PSCB is located
NPA0	26	Next PSCB address: pointer to next PSCB in the
		tree for 0-part of PSCB
NBT0	8	Next bit to test for 0-part of PSCB
LCBA0	26	Leaf control block address: pointer to leaf
		for 0-part of PSCB
NPA1	26	Next PSCB address: pointer to next PSCB in the
		tree for 1-part of PSCB
NBT1	8	Next bit to test for 1-part of PSCB
LCBA1	26	Leaf control block addess: pointer to leaf
		for 1-part of PSCB
Index	8	Index of this PSCB (physically stored in the
	Ì	previous PSCB)
PatBit	1	The value of HashedKey[Index], based on the
1		value of the Index field in the PSCB register
		<u></u>

FIG. 14

		
Field Name	Length	Description
NLARope	4 bytes	Leaf chaining pointer, aging information and direct leaf information
Prefix_Len	1 byte	This field is not used by the TSE for FM trees and can be used by picocode
pattern	2 - 18 bytes	Pattern to be compared with the HashedKey
UserData	variable	The contents of this field is under complete picocode control; the UserData field can include one or more counters

FIG. 15 14/14

